

OM SENI

ESD351DPYR

1-Line Uni-directional ESD DIODE

Features

- Low operating voltage: 3.6V
- Ultra low capacitance: 0.4pF
- Ultra low leakage: nA level
- Low clamping voltage
- -IEC 61000-4-2 (ESD) immunity test
Air discharge: $\pm 30\text{kV}$
Contact discharge: $\pm 30\text{kV}$
- -IEC61000-4-4 (EFT) 40A (5/50ns)
- -IEC61000-4-5 (Lightning) 8A (8/20 μs)
- 2-pin leadless package
- These are Pb-Free Devices
- Response Time is Typically < 1 ns

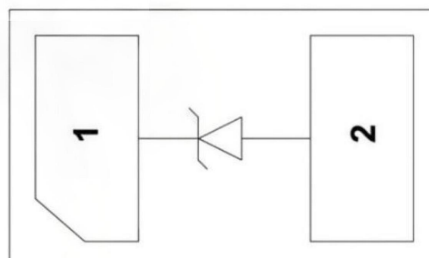
Mechanical Characteristics

- Package: DFN1006-2(0402)
- Lead Finish: Matte Tin
- Case Material: "Green" Molding Compound
- Terminal Connections: See Diagram Below
- -IEC 61000-4-2 (ESD) immunity test

Applications

- Cellular Handsets and Accessories
- Personal Digital Assistants
- Notebooks and Handhelds
- Portable Instrumentation
- USB 2.0 power and data line

Dimensions and Pin Configuration



Marking:DD

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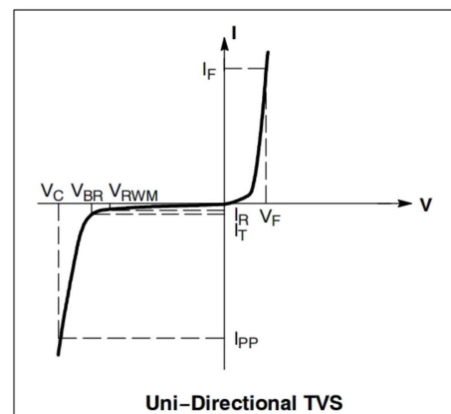
Absolute Maximum Ratings (T_{amb}=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20μs)	P _{pk}	72	W
Peak Pulse Current (8/20μs)	I _{pp}	8	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	VESD	±30 ±30	KV
Operating Temperature Range	T _J	-55 to +125	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

Electrical Characteristics (T_A=25°C unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	VRWM	--	--	3.6	V	
Breakdown Voltage	VBR	4.5	--	--	V	I _T = 1mA
Reverse Leakage Current	I _R	--	--	50	nA	VRWM=3.6V
Clamping Voltage	V _C	--	5.5	7.0	V	I _{pp} =1A(8x 20us pulse)
Clamping Voltage	V _C	--	7.5	10	V	I _{pp} =8A(8x 20us pulse)
Junction Capacitance	C _J	--	0.4	--	pF	V _R = 0V, f = 1MHz

Symbol	Parameter
I _{pp}	Maximum Reverse Peak Pulse Current
V _C	Clamping Voltage @ I _{pp}
V _{RWM}	Working Peak Reverse Voltage
I _R	Maximum Reverse Leakage Current @ V _{RWM}
V _{BR}	Breakdown Voltage @ I _T
I _T	Test Current
I _F	Forward Current
V _F	Forward Voltage @ I _F
P _{pk}	Peak Power Dissipation
C	Capacitance @ V _R = 0 and f = 1.0 MHz



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Characteristic Curves

Fig1. 8/20 μ s Pulse Waveform

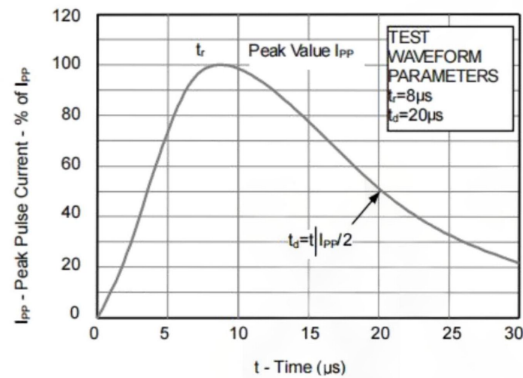


Fig2. ESD Pulse Waveform (according to IEC 61000-4-2)

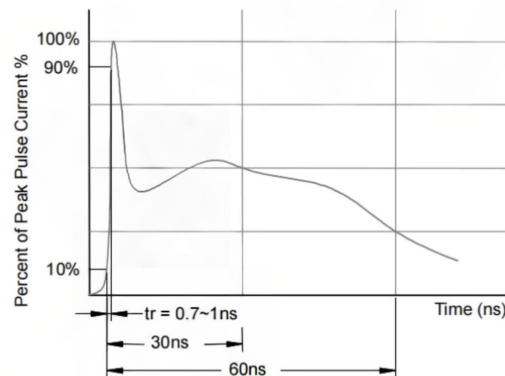
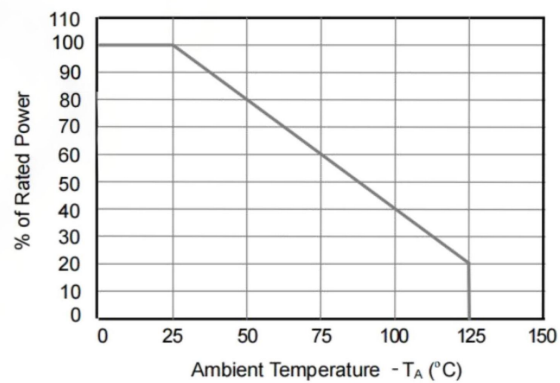


Fig3. Power Derating Curve



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Ordering Information

PREFERRED P/N	PACKAGE	SIZE(mm)	DELIVERY MODE	MPQ(PCS)
	DFN1006-2L	1.00×0.60×0.37	7" REEL	10,000

Recommended Soldering Conditions



Package Outline Dimensions (DFN1006-2L)

Symbol	Dimensions			
	Millimeters		Inches	
	Min	Max	Min	Max
A	0.950	1.050	0.037	0.041
B	0.550	0.650	0.022	0.026
C	0.350	0.450	0.014	0.018
D	0.550	0.650	0.022	0.026
D ₁	0.280	0.380	0.011	0.015
E	0.450	0.550	0.018	0.022
L	0.000	0.100	0.000	0.004

Suggested Pad Layout

Symbol	Dimensions			
	Millimeters		Inches	
	Min	Max	Min	Max
J	0.50	0.60	0.020	0.024
K	0.25	0.35	0.010	0.014
M	0.55	0.65	0.022	0.026